

1 ONITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

TI-35149

Theodore W. Houston

Examiner: TBD

Serial No: 10/635,185

Art Unit: 2816

Filed:

08/06/03

For:

Asymmetric Static Random Access Memory Device Having Reduced Bit

Line Leakage

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97 & 1.98

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)
I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313·1450.

Karen Vertz

1-18-05

Dear Sir:

In the matter of the above-identified application, Applicants are submitting herewith copies of documents listed in the attached form equivalent to Form PTO-1449 for the Examiner's consideration.

In accordance with 37 CFR 1.97 (c), this Information Disclosure statement is being submitted before the mailing date of either a Final Action or a Notice of Allowance.

Furthermore, in accordance with 37 CFR 1.97(e)(1), I hereby certify that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.

Pursuant to 37 CFR 1.97(d), Applicants respectfully petition that this Information Disclosure Statement be considered by the Examiner.

Please charge any fees due in connection with the filing of this paper to the Deposit Account No. 20.0668 of Texas Instruments Incorporated, and please credit any excess fees to such Deposit Account. This form is submitted in triplicate.

Respectfully submitted,

Rose Alyssa Keagy

Attorney for Applicants

Reg. No. 35,095

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Approved for use through 10/31/2002. OMB 0651-0031
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	Substitute for	Form 1449A/PTO		Application Number	10/635,185			
	INFORMATIO	N DISCLOSURE		Filing Date	08/06/03			
		BY APPLICANT		First Named Inventor	Theodore W. Houston			
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	(use as many s	heets as necessary)		Examiner Name	TBD			
Sheet	1	of	1	Attorney Docket No.	TI-35149			

				U.S. I	PATENT DOCUM	ENTS
Exam. Initials*	Cite No.1	U.S. Patent Document		Name of Patentee	Date of Pub.	
		Number	Kind Code ² (if known)	or Applicant of Cited Doc.	of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	AA	4,584,671		Monk	4/22/86	
	AB	2003/0076721		Forbes	4/24/03	
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Exam. Initials*	Cite No.1	Foreign Patent Document			Name of Patentee	Date of Pub.		
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	BE							
	BF							
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	OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS					
Exam. Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²			
	CA	NAVID AZIZI, ET AL, "Low-Leakage Asymmetric-Cell SRAM" Low Power Electronics and Design 2002, August 12, 2002, pp. 48-51				
	СВ					
	СС					
	CD	'				
	CE					
	CF					
	CG					

Examiner	Date	
Signature	Considered	

US and Foreign Patent Documents: ¹Unique citation designation number. ²See attached Kinds of U.S. Patent Documents. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

Other Prior Art/Non-Patent Literature Documents: ¹Unique citation designation number. ²Applicant is to place a check mark here if English Translation is attached.

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.